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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

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CENTRAL INTELLIGENCE AGENCY

Applicant Andrew Marshall

Docket Number: TI-34177

DEC 22 2004

Serial No.: 10/626,438

Art Unit: 2818

Filed: 07/24/03

Examiner: Thong Q. Le

For: Circuit for Reducing Standby Leakage in a Memory Unit

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NAME OF INVENTOR(S): Andrew Marshall	
TITLE OF INVENTION: Circuit for Reducing Standby Leakage in a Memory Unit	
TI FILE NO.: <b>TI-34177</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
FAXED: 12-22-04 DUE: 12-22-04 ATTY/SECY: AKS/tjv	
RECEIPT DATE & SERIAL NO.: Serial No.: <b>10/626,438</b> Filing Date: 07/24/03	

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Applicant: Andrew Marshall

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For: CIRCUIT FOR REDUCING STANDBY LEAKAGE IN A MEMORY UNIT

Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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Karen Vertz  
Karen Vertz

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AMENDMENT

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